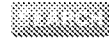




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### 1 [Simulation environment to assess technology insertion impact and optimized manning](#)

Niraj Srivastava, Frank Pietryka, Gary Horne, Mark Therooff

December 2005 **WSC '05**: Proceedings of the 37th conference on Winter simulation**Publisher:** Winter Simulation ConferenceFull text available: [pdf\(403.45 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

The reduction in life-cycle costs for Naval vessels is critical for operating a cost efficient and robust Navy. Computer based simulations are an effective tool for human system integration optimization, as well as for studying the risks associated with ...

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### 2 [Parallel discrete event simulation](#)



Richard M. Fujimoto

October 1990 **Communications of the ACM**, Volume 33 Issue 10**Publisher:** ACM
 Full text available: [pdf\(7.32 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Parallel discrete event simulation (PDES), sometimes called distributed simulation, refers to the execution of a single discrete event simulation program on a parallel computer. PDES has attracted a considerable amount of interest in recent years. From ...

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### 3 [Parallel and distributed simulation: managing external workload with BSP time warp](#)

Malcolm Yoke Hean Low

December 2002 **WSC '02**: Proceedings of the 34th conference on Winter simulation: exploring new frontiers**Publisher:** Winter Simulation ConferenceFull text available: [pdf\(173.52 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper describes an extension to the existing BSP Time Warp dynamic load-balancing algorithm to allow the management of interruption from external workload. Experiments carried out on a manufacturing simulation model using different partition strategies ...

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[netflowanalyzer.com/Netfi](#)

#### 4 [Performance analysis of a system of communicating processes](#)

Sujit Dey, Surendra Bommur

November 1997 **ICCAD '97: Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

**Publisher:** IEEE Computer Society

Full text available:  [pdf\(148.80 KB\)](#)  [Publisher Site](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Efficient exploration of the system design space necessitates fast and accurate performance estimation as opposed to the computationally prohibitive alternative of exhaustive simulation. The paper addresses the issue of worst case performance analysis ...

**Keywords:** PERC, accurate worst case performance analysis technique, communicating sequential processes, concurrent communicating processes, exhaustive simulation, inter process communication, multiple communicating processes, performance analysis, performance estimation, single process descriptions, synchronization overhead, system design space, system implementation, system of communicating processes, system performance, worst case performance analysis, worst case performance estimate


#### 5 [A multiprocessor DSP system using PADDI-2](#)



Roy A. Sutton, Vason P. Srinani, Jan M. Rabaey

May 1998 **DAC '98: Proceedings of the 35th annual conference on Design automation**

**Publisher:** ACM

Full text available:  [pdf\(179.50 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

We have integrated an image processing system built around PADDI-2, a custom 48 node MIMD parallel DSP. The system includes image processing algorithms, a graphical SFG tool, a simulator, routing tools, compilers, hardware configuration and debugging ...


#### 6 [Optimal allocation of on-chip memory for multiple-API operating systems](#)



D. Nagle, R. Uhlig, T. Mudge, S. Sechrest

April 1994 **ACM SIGARCH Computer Architecture News**, Volume 22 Issue 2

**Publisher:** ACM

Full text available:  [pdf\(1.27 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

The allocation of die area to different processor components is a central issue in the design of single-chip microprocessors. Chip area is occupied by both core execution logic, such as ALU and FPU datapaths, and memory structures, such as caches, TLBs, ...

#### 7 [Simulation to support operational testing: a practical application](#)



Bradford S. Canova, Peter H. Christensen, Michael D. Lee, Bruce R. Tripp, Michael H. Pack, David L. Pack

December 1999 **WSC '99: Proceedings of the 31st conference on Winter simulation: Simulation---a bridge to the future - Volume 2**, Volume 2

**Publisher:** ACM


Full text available:  [pdf\(158.38 KB\)](#) Additional Information: [full citation](#), [index terms](#)

## 8 [Web-based simulation 2: the ABELS system: designing an adaptable interface for linking simulations](#)

G. Ayorkor Mills-Tettey, Greg Johnston, Linda F. Wilson, Joseph M. Kimpel, Bin Xie

December 2002 **WSC '02: Proceedings of the 34th conference on Winter simulation: exploring new frontiers**

**Publisher:** Winter Simulation Conference

Full text available:  [pdf\(211.22 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

The Agent-Based Environment for Linking Simulations (ABELS) provides a framework to facilitate the dynamic exchange of data between distributed simulations and other remote data resources. Specifically, the framework allows the formation of a dynamic ...


## 9 [A case study of verification, validation, and accreditation for advanced distributed simulation](#)



Ernest H. Page, Bradford S. Canova, John A. Tufarolo

July 1997 **ACM Transactions on Modeling and Computer Simulation (TOMACS)**, Volume 7 Issue 3

**Publisher:** ACM

Full text available:  [pdf\(501.51 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

The techniques and methodologies for verification and validation of software-based systems have arguably realized their greatest utility within the context of simulation. Advanced Distributed Simulation (ADS), a major initiative within the defense modeling ...


**Keywords:** IDEF modeling, advanced distributed simulation, aggregate level simulation protocol, life cycle, validation and accreditation, verification, wargame

## 10 [Simulation-based engineering of complex systems: simulation-based engineering of complex systems using EXTEND+MFG+OpeMCSS](#)

John R. Clymer

December 2002 **WSC '02: Proceedings of the 34th conference on Winter simulation: exploring new frontiers**

**Publisher:** Winter Simulation Conference

Full text available:  [pdf\(297.11 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

A Complex Adaptive System (CAS) is a network of self-organizing, intelligent agents that share knowledge and adapt their operations in order to achieve overall system goals. Three things are needed to understand, design, and evaluate CAS. First, a mathematical ...


## 11 [Composition and refinement of discrete real-time systems](#)



Jonathan S. Ostroff

January 1999 **ACM Transactions on Software Engineering and Methodology (TOSEM)**, Volume 8 Issue 1

**Publisher:** ACM

Full text available:  [pdf\(1.59 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Reactive systems exhibit ongoing, possibly nonterminating, interaction with the environment. Real-time systems are reactive systems that must satisfy

quantitative timing constraints. This paper presents a structured compositional design method for discrete ...


**Keywords:** abstraction, model-checking, modules, refinement, state explosion, temporal logic, timed logic

## 12 [Parallel simulation: distributed simulation systems](#)

Richard M. Fujimoto

December 2003 **WSC '03**: Proceedings of the 35th conference on Winter simulation: driving innovation

**Publisher:** Winter Simulation Conference

Full text available:  [pdf\(317.06 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)


An overview of technologies concerned with distributing the execution of simulation programs across multiple processors is presented. Here, particular emphasis is placed on discrete event simulations. The High Level Architecture (HLA) developed by the ...

## 13 [The architecture and performance of security protocols in the ensemble group communication system: Using diamonds to guard the castle](#)



August 2001 **ACM Transactions on Information and System Security (TISSEC)**, Volume 4 Issue 3

**Publisher:** ACM

Full text available:  [pdf\(418.73 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Ensemble is a Group Communication System built at Cornell and the Hebrew universities. It allows processes to create *process groups* within which scalable reliable fifo-ordered multicast and point-to-point communication are supported. The system ...

**Keywords:** Group communication, security


## 14 [Caching constrained mobile data](#)



Subhasish Mazumdar, Mateusz Pietrzyk, Panos Chrysanthis

October 2001 **CIKM '01**: Proceedings of the tenth international conference on Information and knowledge management

**Publisher:** ACM

Full text available:  [pdf\(1.67 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

As mobile devices get ubiquitous and grow in computational power, their management of interdependent data also becomes increasingly important. The mobile environment exhibits all the characteristics of a distributed database plus the feature of whimsical ...


## 15 [Bounded concurrent timestamp systems using vector clocks](#)



Sibsankar Haldar, Paul Vitányi

January 2002 **Journal of the ACM (JACM)**, Volume 49 Issue 1

**Publisher:** ACM

Full text available:  [pdf\(330.18 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Shared registers are basic objects used as communication mediums in asynchronous concurrent computation. A concurrent timestamp system is a higher typed communication object, and has been shown to be a powerful tool

to solve many concurrency control ...

**Keywords:** Concurrent reading while writing, label, labeling and scan, nonatomic operation execution, operation execution, operation---read and write, regular and atomic, shared variable---safe, timestamp system, traceability, vector clock, wait-freedom

## 16 Compiler-directed run-time monitoring of program data access



Chen Ding, Yutao Zhong

June 2002 **MSP '02**: Proceedings of the 2002 workshop on Memory system performance

**Publisher:** ACM

Full text available: [pdf\(1.40 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Accurate run-time analysis has been expensive for complex programs, in part because most methods perform on all a data. Some applications require only partial reorganization. An example of this is off-loading infrequently used data from a mobile device. ...

## 17 Data modeling in DELAB



Yannis E. Ioannidis, Miron Livny

June 1988 **ACM SIGMOD Record**, Volume 17 Issue 3

**Publisher:** ACM

Full text available: [pdf\(170.43 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

As the size and complexity of processing and manufacturing systems increases, the need for Database Management Systems (DBMS) that meet the special needs of studies that experiment with such systems becomes more current. System analysts who study the ...

## 18 An integrated framework on mining logs files for computing system management



Tao Li, Feng Liang, Sheng Ma, Wei Peng

August 2005 **KDD '05**: Proceeding of the eleventh ACM SIGKDD international conference on Knowledge discovery in data mining

**Publisher:** ACM

Full text available: [pdf\(694.29 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Traditional approaches to system management have been largely based on domain experts through a knowledge acquisition process that translates domain knowledge into operating rules and policies. This has been well known and experienced as a cumbersome, ...

**Keywords:** event relationship, log categorization, system management, temporal pattern

## 19 Experimental evaluation of synchronization and topology control for in-building sensor network applications



W. Steven Conner, Jasmeet Chhabra, Mark Yarvis, Lakshman Krishnamurthy

September 2003 **WSNA '03**: Proceedings of the 2nd ACM international conference on Wireless sensor networks and applications

**Publisher:** ACM

Full text available: [pdf\(1.24 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

While multi-hop networks consisting of 100s or 1000s of inexpensive embedded sensors are emerging as a means of mining data from the environment, inadequate network lifetime remains a major impediment to real-world deployment. This paper describes several ...

**Keywords:** synchronization, topology control, wireless sensor networks

## 20 [Emergent \(mis\)behavior vs. complex software systems](#)



Jeffrey C. Mogul

April 2006 **EuroSys '06**: Proceedings of the ACM SIGOPS/EuroSys European Conference on Computer Systems 2006

**Publisher:** ACM

Full text available: [pdf\(391.85 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Complex systems often behave in unexpected ways that are not easily predictable from the behavior of their components; this is known as *emergent behavior*. As software systems grow in complexity, interconnectedness, and geographic distribution, ...

**Keywords:** complex systems, emergent behavior, emergent misbehavior

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